### **PLA Series**

# **PC-Based Logic Analyzer**

PLA Series (PC-Based logic analyzer) provide digital software and hardware designers to capture and analyze the intangible signals and able to measure the complex circuits precisely. Furthermore, the compact size and via USB ability to connect to a laptop PC makes them an ideal and convenient solution for use at remote sites.

#### Features

- External(asynchronous) and internal (synchronous) capture : Offer a more convenient environment for engineers.
- The timing of each cursor mark to the trigger can be shown.
- Provide binary code and hexadecimal List mode (State) display.
- Able to save the measuring data and waveform completely.
- Offers I<sup>2</sup>C, SPI, UART etc. signal decode function.
- Provide various signal trigger and capture: Pattern / Edge / AND / OR, 4 kinds of trigger modes.
- Pre-trigger, 3 level Post-trigger and continued-trigger functions make user operate more easily.
- Bus analysis and glitch capture function.
- 256K Bytes~2M Bytes long memory depth; each CH memory depth is up to 128Kbits~ 512Kbits
- Able to set the sample rate size by users, which avoid capturing time too long.

- Provide "Trigger Counter" and "Pulse Width Trigger" function.
- Provide High-speed Zoom In / Zoom Out function.
- Smart software provides text file for saving the Binary Code of waveform.
- Connect to PC which able let the user to view, save, analyze and printout the data
- No need extra power, just connect to the USB.

#### Application

- Development, measurement and quality control for digital products.
- Electronic, Electric machinery, Communication, Biotechnology and Medical digital products.

### **Physical Specification**

Timing 100MHz,Max 250MHz State 100MHz Bandwidth
State 100MHz
100MHz
Bandwidth
Danuwiuth
$100 \mathrm{MHz}$
Channels
16,32CH
RAM Size
256K~2M Bytes
Storage Depth per Channel
128K~512K bits x 16 or 32CH
Maximum Input Voltage
$\pm 30 \mathrm{V}$
Threshold Range
$-4V \sim +4V$
Support PC-Link function
For Win-98/2k/XP
PC-Link Interface
USB 2.0
Power
USB
Temperature
$0^{\circ}$ C $\sim$ 45 $^{\circ}$ C
Dimension
15(W) x 8(D) x 3(H) cm
Weight

#### **Standard Accessories**

Main Unitx1
Lead Set
16CHx1
32CHx2
CDx1
(PC-Link software & User's manual)
USB Cablex1

#### More product information

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## Leaptronix®

Provide the best measurement solution

### **PLA Series Product Specification**

Mode	PLA-1016	PLA-2532
Timing Analysis	$100 \mathrm{MHz,max}(10 \mathrm{ns})$	250MHz,max(4ns)
State Analysis	100MHz(Max)	100MHz(Max)
Bandwidth	$100 \mathrm{MHz}$	100MHz
Channels	16CH	32CH
MEMORY		
RAM Size	256K Bytes	2M Bytes
Storage Depth per Channel	128Kbits x 16CH	512Kbits x 32CH
TRIGGER		
Condition	Pattern / Edge / AND / OR	Pattern / Edge / AND / OR
Channels	16CH	32CH
Pre/Post Trigger	YES	YES
Trigger Levels	3 (Edge or Pattern)	3 (Edge or Pattern)
Continue Trigger	YES	YES
Trigger out	YES (TTL Level)	YES (TTL Level)
Trigger Counter	$1 \sim 255$	1~255
Pulse Width Trigger	YES	YES
Bus Analysis	YES	YES
Glitch Capture	YES	YES
Threshold Range		
Range	$-4V \sim +4V$	-4V~+4V
Acuracy	$\pm 50 \mathrm{mV}$	$\pm 50 \mathrm{mV}$
Maximum Input Voltage	$\pm 30 \mathrm{V}$	$\pm 30 \mathrm{V}$
Impedance	$100 { m K}\Omega$ shunted by $pprox$ 10pF	$100 { m K}\Omega$ shunted by $pprox 10 { m pF}$
Temperature		
Operating	$0^{\circ}$ C $\sim$ 45 $^{\circ}$ C(41 $^{\circ}$ F $\sim$ 113 $^{\circ}$ F)	$0^\circ\!\mathbb{C}\!\sim\!\!45^\circ\!\mathbb{C}(41^\circ\!\mathbb{F}\!\sim\!\!113^\circ\!\mathbb{F})$
Storage	-40°C~75°C(-56°F~167°F)	-40°C~75°C(-56°F~167°F)
Data Skew(Channel to Channel)	10ns typical	10ns typical
PC-Link Interface	USB 2.0	USB 2.0
Power	USB	USB
Dimension		
(W) x (D) x (H) cm	15(W) x 8(D) x 3(H) cm	15(W) x 8(D) x 3(H) cm
Weight	230g	240g

